

**Amendments to the Specification:**

*Please replace paragraph [1007], beginning at page 2, with the following paragraph [1007], marked to show changes:*

One reasonably comprehensive hardware assisted profiling environment is provided by the Digital Continuous Profiling Infrastructure (DCPI) tools that run on Alpha processor systems to provide profile information at several levels of granularity, from whole images down to individual procedures and basic blocks on down to detailed information about individual instructions, including information about dynamic behavior such as cache misses, branch mispredicts and other forms of dynamic stalls. ~~Detailed information on the DCPI tools and downloadable code may be found (at least as of the filing date) at~~ <http://h30097.www3.hp.com/dcp/>. ~~Additional descriptive information~~ Information on the DCPI tools appear[[s]] in Jennifer Anderson, Lance Berc, George Chryos, Jeffrey Dean, Sanjay Ghemawat, Jamey Hicks, Shun-Tak Leung, Mitch Lichtenberg, Mark Vandevoorde, Carl A. Waldspurger, William E. Weihl, "Transparent, Low-Overhead Profiling on Modern Processors," in Proceedings of the Workshop on Profile and Feedback-Directed Compilation in conjunction with the International Conference on Parallel Architectures and Compilation Techniques (PACT 98), Paris, France (October 13, 1998).